

Fig. 1 (PRIOR ART)

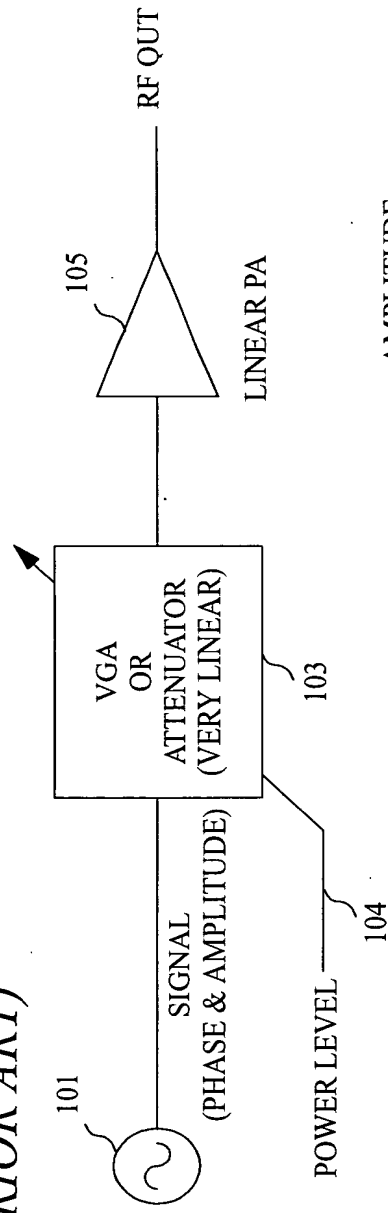


Fig. 2

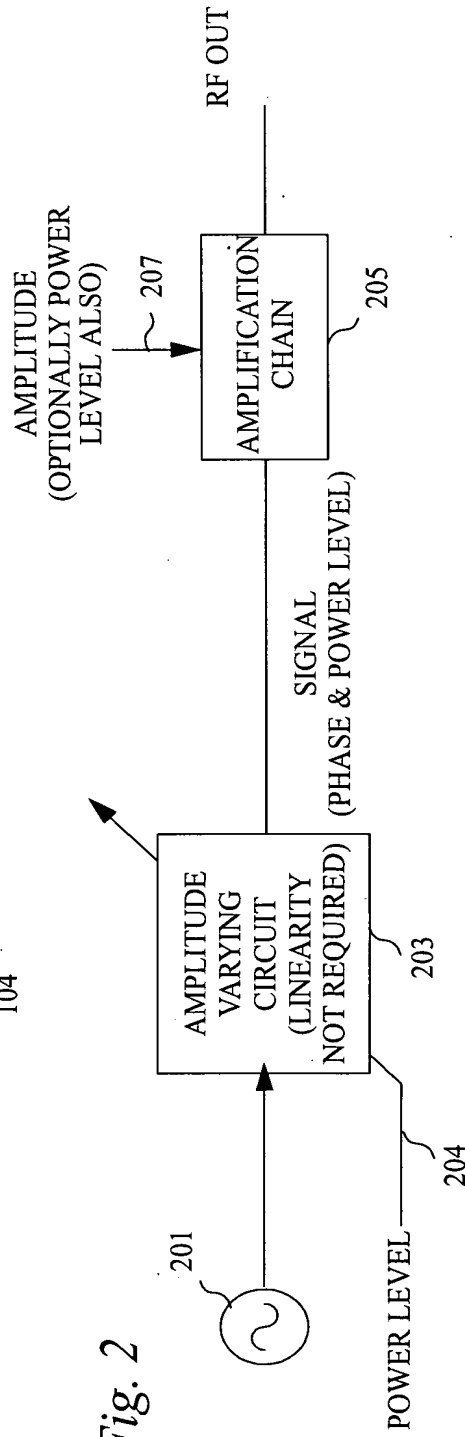


Fig. 3

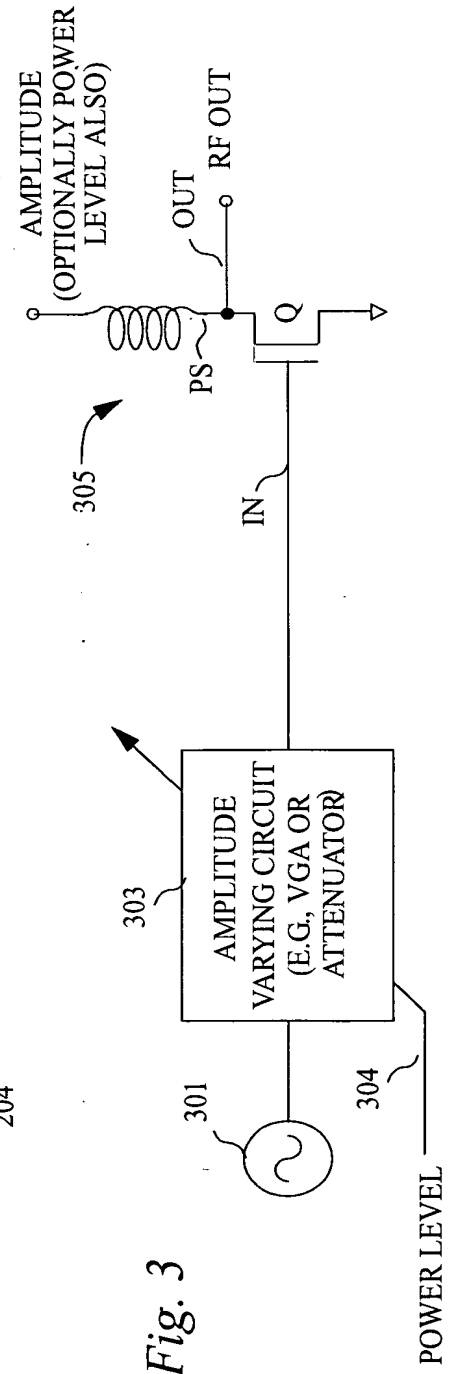


Fig. 4

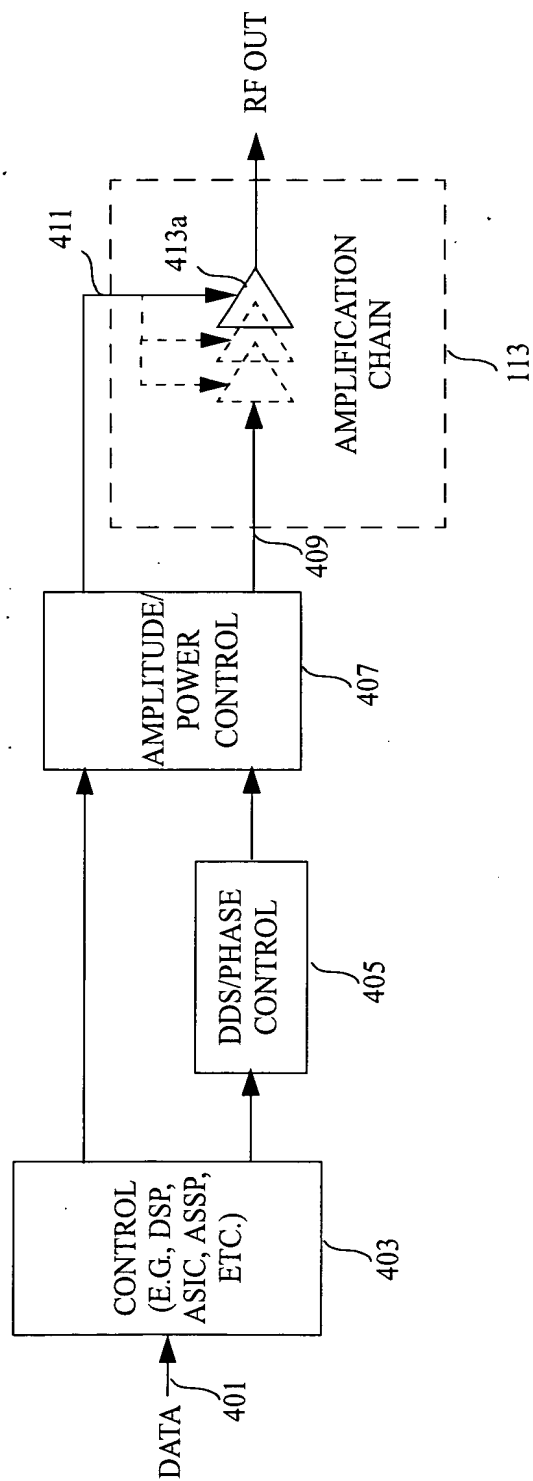


Fig. 5

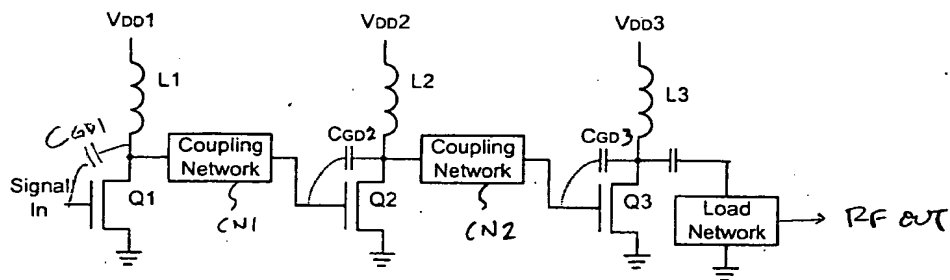


Figure 1

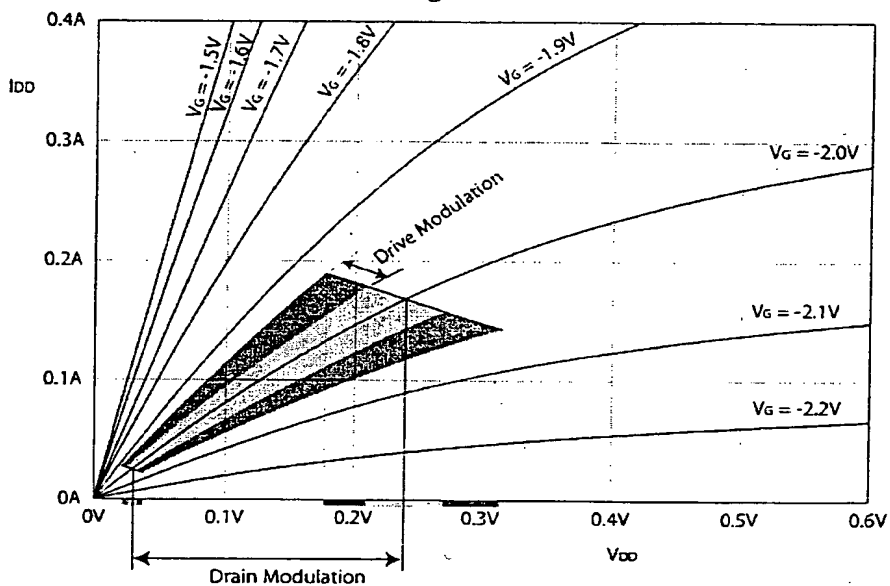
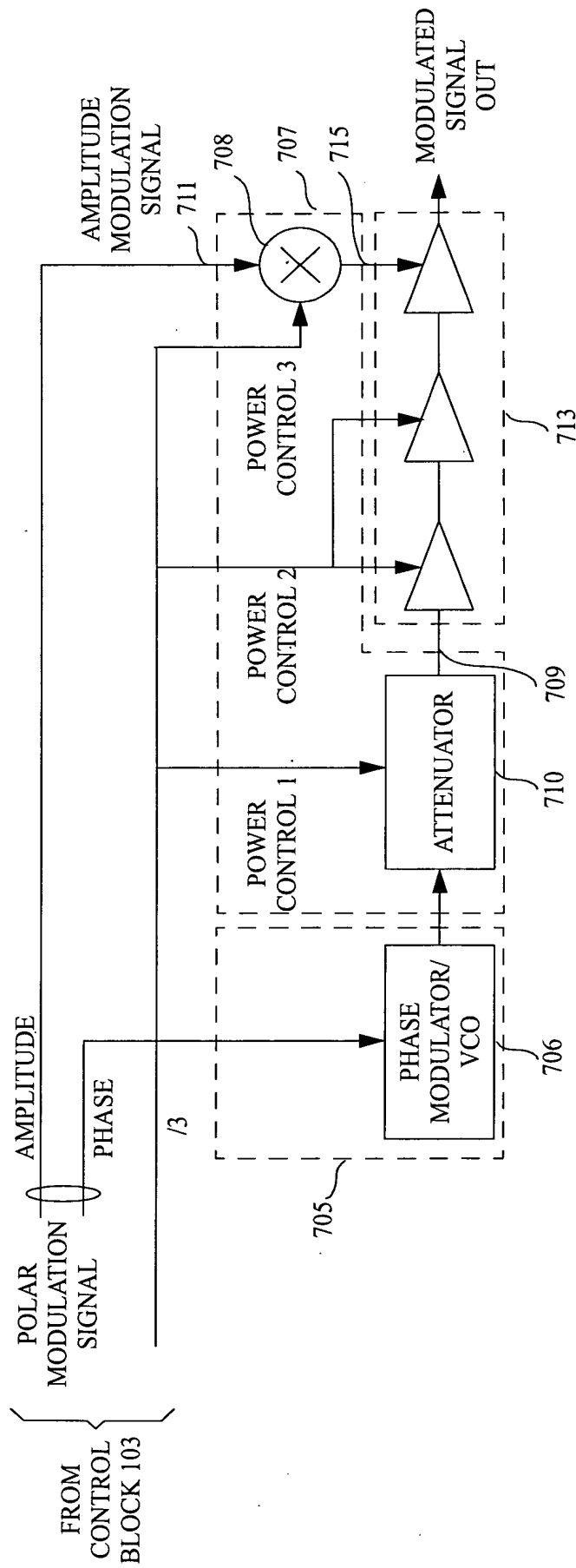


Fig. 6

For the technology

Fig. 7



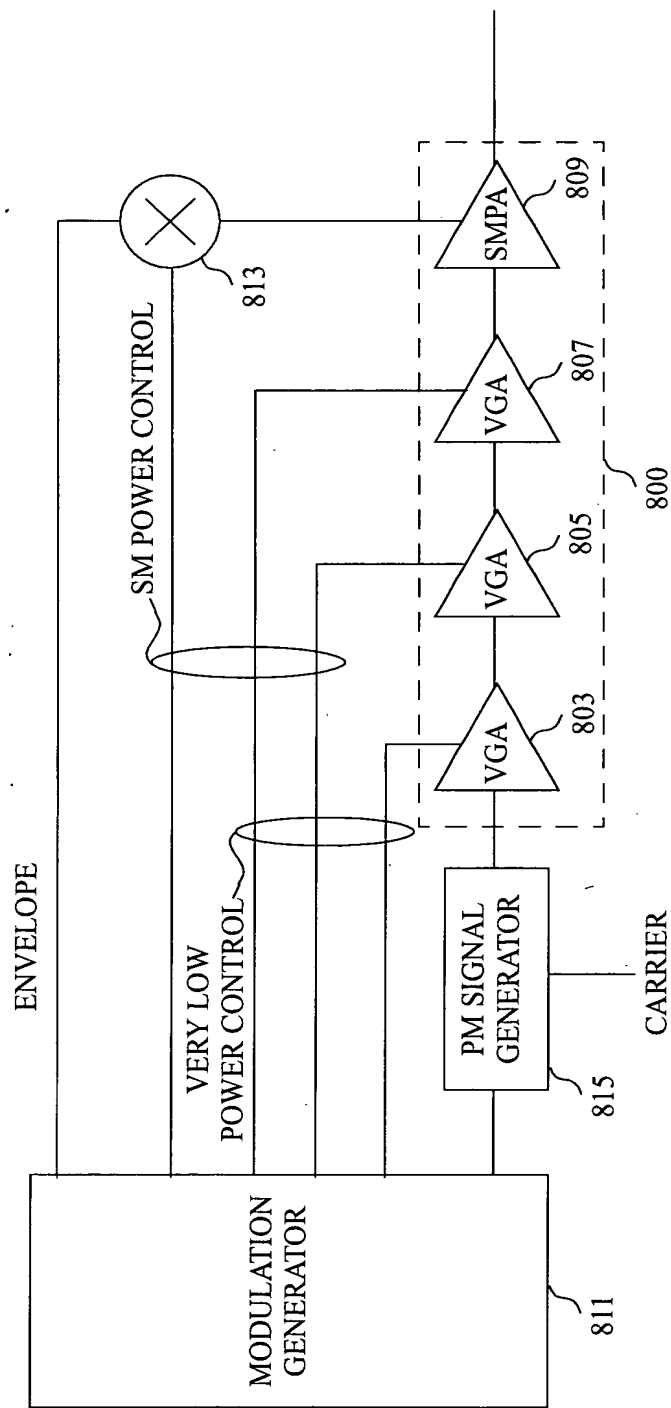


Fig. 8

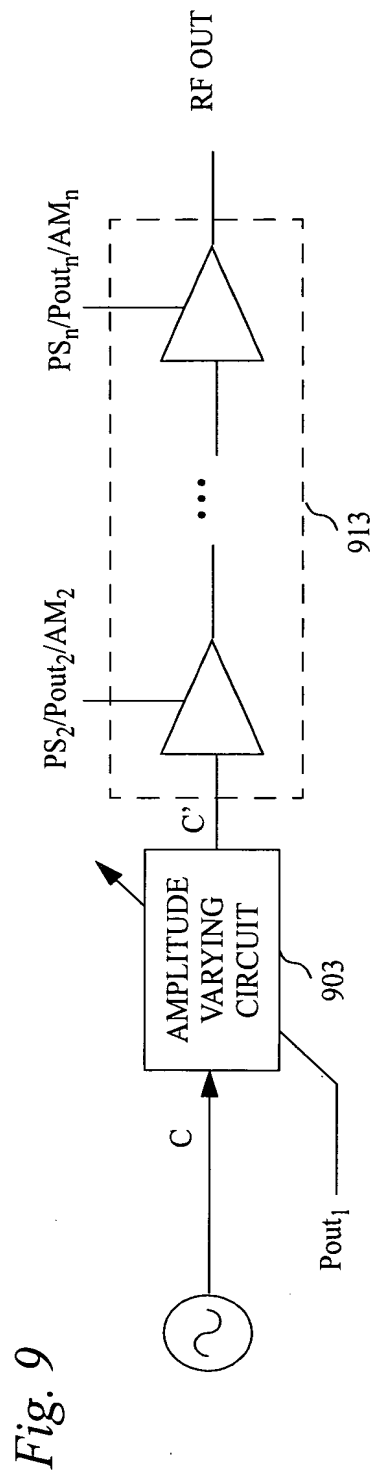


Fig. 9

Fig. 10

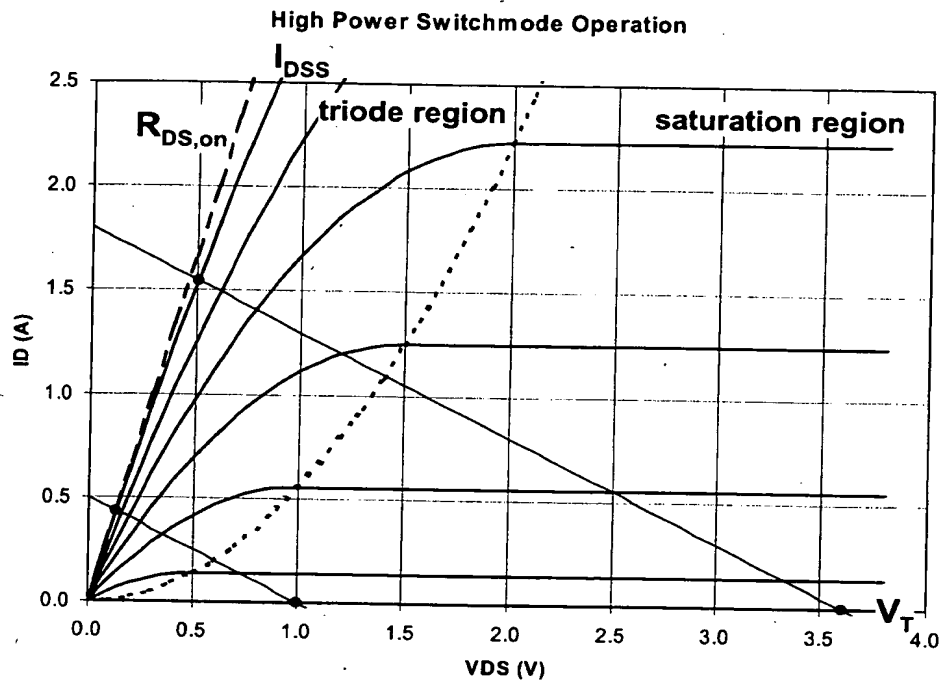
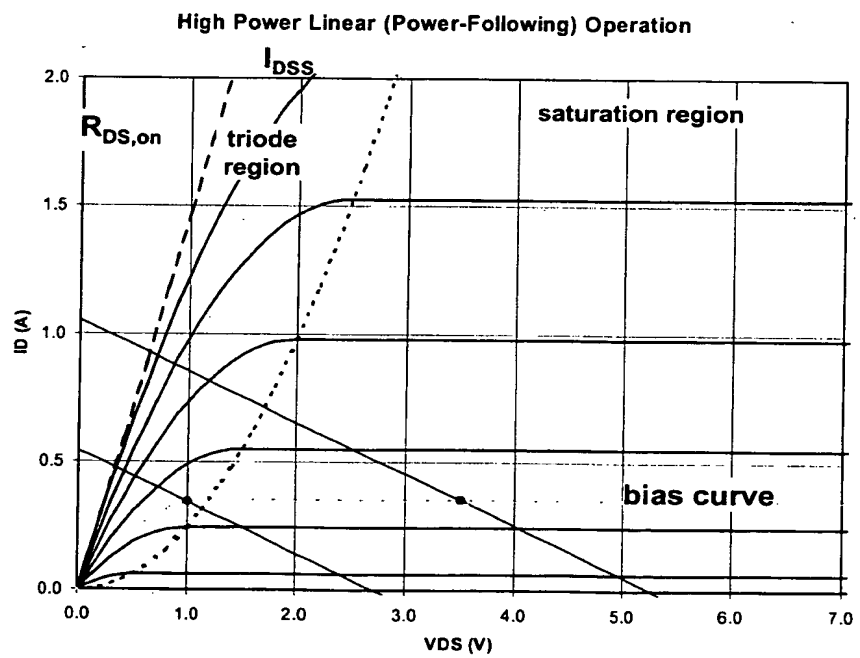


Fig. 11



TO THE "THEORETICAL"

Fig. 12

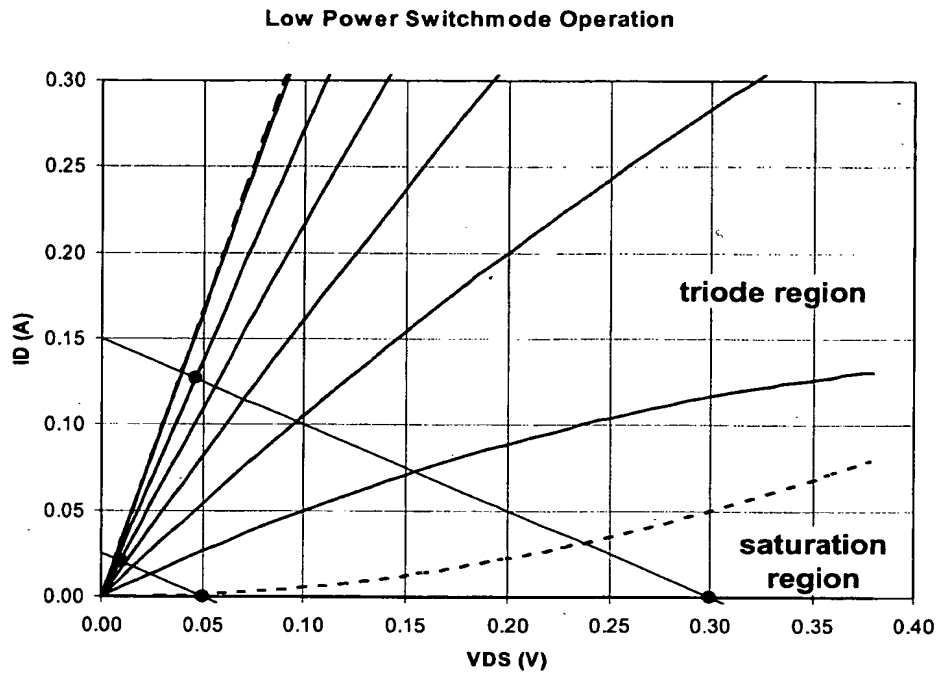
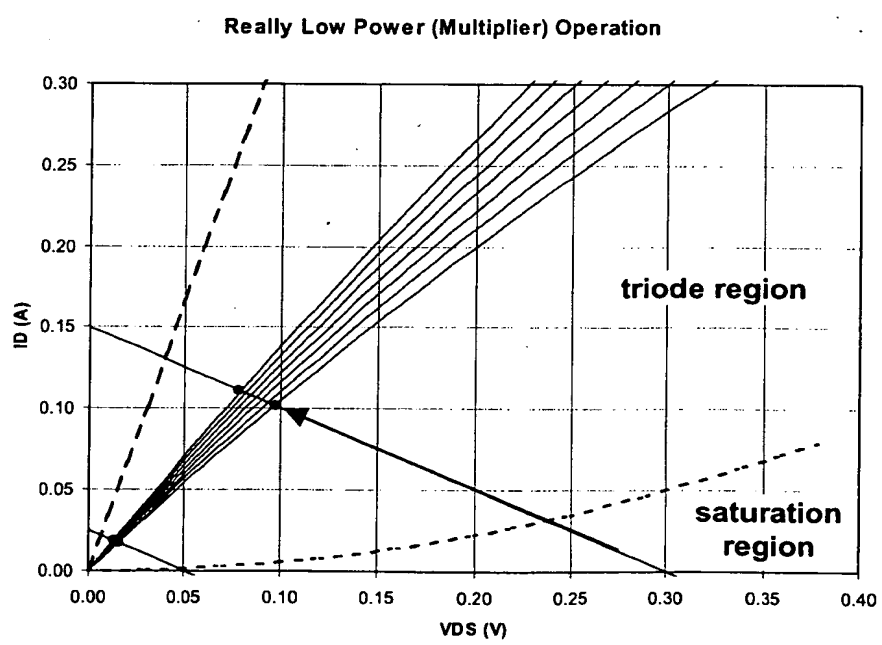
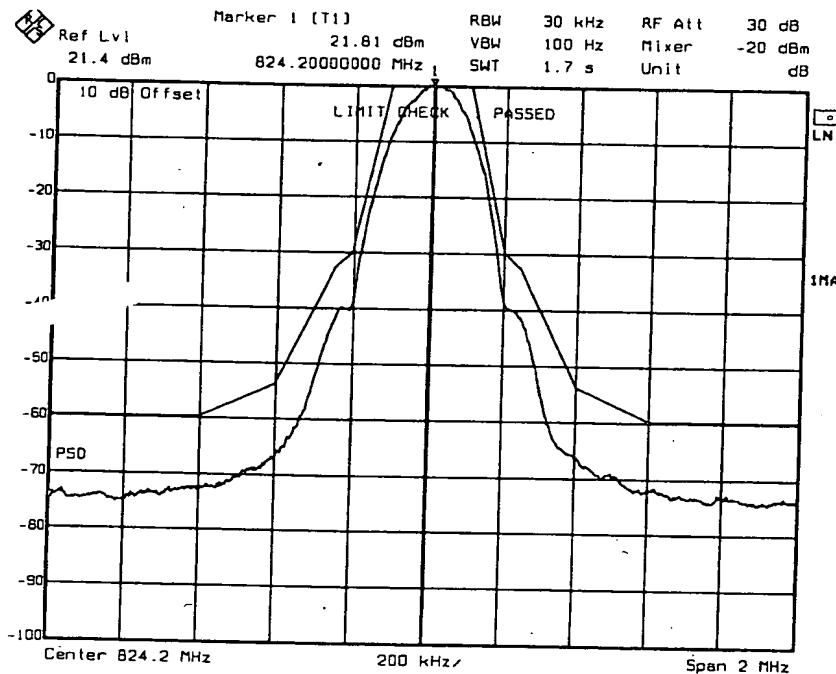


Fig. 13

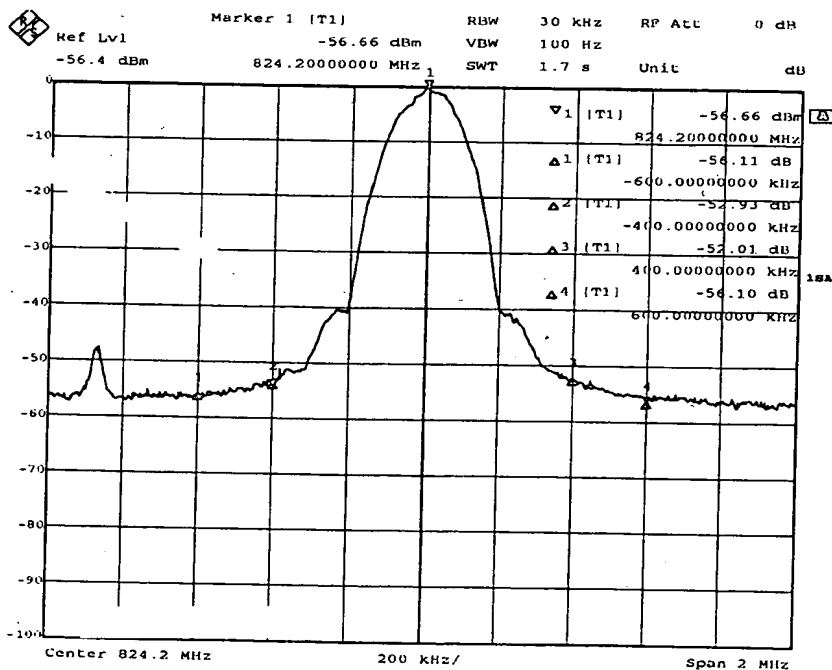


The graph plots drain current I_D (A) on the y-axis (0.00 to 0.30) against drain-source voltage V_{DS} (V) on the x-axis (0.00 to 0.40). A family of solid curves represents different V_{GS} values. A dashed line indicates the $<5\%$ linearity region, and another dashed line indicates the saturation region. A solid line with a negative slope is also shown.



30 dBm rms
 EVM = 1.8%

Fig. 15



-50 dBm rms
 EVM = 2.3%

Fig. 16